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U.S. UTILITY Patent Application

PATENT NUMBER and  
ISSUE DATE

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10016863	12/14/2001	714	718	2133	<i>Decady</i>

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*RSP*

\*\*CONTINUING DATA VERIFIED:

\*\* FOREIGN APPLICATIONS VERIFIED:  
GERMANY 100 62 404.9 12/14/2000

PG-PUB DO NOT PUBLISH ☐

RESCIND ☐

Foreign priority claimed ☐ yes ☐ no

35 USC 119 conditions met ☐ yes ☐ no

Verified and Acknowledged Examiners's initials

ATTORNEY DOCKET NO

W&B-INF-908

TITLE : Device and method for reducing the number of addresses of faulty memory cells

U.S. DEPT. OF COMM/PAT & TM-PTO-415L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg.
		Print Fig.	
TERMINAL DISCLAIMER		Application Examiner	
		PREPARED FOR ISSUE	
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☐ CD-ROM

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